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High-performance control of a three-phase voltage-source converter including feedforward compensation of the estimated load current

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ABSTRACT

In this paper a new control strategy for voltage-source converters (VSC) is introduced. The proposed strategy consists of a nonlinear feedback controller based on feedback linearization plus a feedforward compensation of the estimated load current. In our proposal an energy function and the direct-axis current are considered as outputs, in order to avoid the internal dynamics. In this way, a full linearization is obtained via nonlinear transformation and feedback. An estimate of the load current is feedforwarded to improve the performance of the whole system and to diminish the capacitor size. This estimation allows to obtain a more rugged and cheaper implementation. The estimate is calculated by using a nonlinear reduced-order observer. The proposal is validated through different tests. These tests include performance in presence of switching frequency, measurement filters delays, parameters uncertainties and disturbances in the input voltage.

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1. Introduction

In the last decades, the advance in semiconductor devices allowed the use of power converters in many industrial applications. Voltage-source converters (VSCs) are often used as voltage sources. The main advantages of PWM-VSCs are: unity power factor, sinusoidal input current, high performance of the DC-voltage control, bi-directional power flow, and low total harmonics distortion index. Due to these features VSCs are a good option to be used in industrial drives [1,2], when a source with a highly controlled DC-voltage level is needed. In addition, it must be indicated that the VSC is used in many power applications, since it is an element appearing in more complex topologies (e.g. Active Power Filters, Back-to-Back Converter and Universal Power Quality Conditioner (UPQC) [3-5]). In other applications, such as Flexible AC Transmission Systems (FACTS), VSC is used as a component of different topologies (e.g. Static Synchronous Compensator (STATCOM), Static Synchronous Series Compensator (SSSC), Unified Power Flow Controller (UPFC), and High-Voltage Direct Current (HVDC) [6-8]). Besides, VSCs often appear in a lot of distributed power generation and alternative energy applications [9–15]. Consequently, it is very important to develop control strategies for VSCs satisfying high-performance conditions.

It must be noted that VSC model is nonlinear, multi-inputmulti-output and under-actuated. In order to control VSCs, several control techniques with different performances can be found in the literature. For example, a strategy based on Jacobian linearization around an equilibrium point has been introduced in [16,17]. However, this strategy presents some drawbacks. Among others, operation range is restricted and a relatively big capacitor is needed for keeping a constant DC-voltage in presence of a varying load. For this reason, nonlinear approaches have been proposed by other researchers. Some of them are based on unstructured controllers (i.e. fuzzy logic and neural networks), although VSC model is highly structured [18]. A back-stepping control was presented in [19], whereas passivity based control can be found in [20]. Also, input-output linearization based on feedback linearization has been applied [21]. This technique transforms the nonlinear system into a linear decoupled one. Then linear control laws can be used for guaranteeing stability in the whole operation range.

It is very important to remark that when DC-voltage is selected as an output, a nonlinear uncontrollable internal dynamics appears (see [22]). In order to overcome this drawback, fast current inner loops, by using feedback linearization taking into account currents as outputs, are generally considered. Then, a slower outer DC-voltage loop is designed via a PI-based cascade control [21–26]. Other options considering simplified models were presented in [27,28]. In both papers a model neglecting losses and reactive energy was considered. In [28] the VSC was loaded with a constant resistance and the square of the DC-voltage was taken as a state to be controlled. In this way, the simplified models allow to implement

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a full input-output linearization eliminating the presence of uncontrollable internal dynamics.

VSCs often feed varying loads (either linear or nonlinear). In such cases, feedforward compensator can be designed to improve the controller performance. It is well-known, that feedforward compensation needs more sensors for sensing the variables to be feedforwarded. Many times, designers prefer to avoid extra sensors. In such a case, a common practice consists in replacing the actual sensor by a software sensor (for instance, an observer). By using an observer-based control strategy several advantages appear. The controller is more rugged, more reliable and, also the hardware is less expensive. Moreover, in many applications the current load may contain high frequencies being more convenient to design a mean value estimator, than to obtain a true measurement [29].

In [26,29] software sensors based on open-loop predictors were introduced. In order to improve the performance of open-loop predictors, closed-loop predictors can be designed. Flatness theory has been used to design a nonlinear controller for VSCs loaded with a constant power source and a nonlinear observer has been employed for estimating the unknown load power [30]. The proposed nonlinear observer uses Lyapunov theory to calculate the observer gains. In [24] the load is modeled as a variant resistance and its value is estimated using a full-order observer. However, in order to diminish the computational burden and peaking phenomenon sensibility, a nonlinear reduced-order observer [31] can be built for estimating the load current.

In this paper a new control strategy is introduced. The controller combines a feedback nonlinear law with a feedforward compensation. The feedback law is based on feedback linearization technique where outputs (an energy function and direct-axis current) are chosen in order to avoid the internal dynamics without model simplifications. In this way, a decoupled linear system is attained. For this reason, the proposed technique avoids the drawback appearing in other proposals due to the coupling between the inner current loop and the outer voltage loop.

The feedforward compensation is used for improving the whole system performance. In addition, it must be noted that a smaller capacitor can be used for maintaining the performance obtained with controllers proposed in other works. The load current is estimated via a reduced-order observer. The proposed observer guarantees linear error dynamics so that the convergence of the estimation error is exponential. This is a very important merit of the proposed observer, since the estimates are used in the control loop.

The paper is organized as follows. In Section 2 the VSC model and control strategy design is described. In Section 3, a reduced-order observer for estimating the load current is designed. Performance evaluation, discussion and tests are presented in Section 4. Finally, conclusions are drawn in Section 5.

2. Control strategy without internal dynamics

2.1. Voltage-source converter model

The VSC model in a rotating reference frame *dq* is given by [16],

$$Li_d = -Ri_d - L\omega i_q + \eta_d v_{dc} - v_d, \tag{1}$$

$$Li_q = -Ri_q + L\omega i_d + \eta_q v_{dc} - v_q, \qquad (2)$$

$$C_{dc} \dot{\nu}_{dc} = -\frac{3}{2} (\eta_d i_d + \eta_q i_q) + i_L, \tag{3}$$

where i_d , i_q , v_d , and v_q are the line currents (i_a , i_b , i_c) and phase voltages (v_a , v_b , v_c) transformed to the dq reference frame, η_d and η_q represent the control signals, v_{dc} is the DC-voltage, and i_L is the load current. Parameters can be seen in Fig. 1, where the schematic electric circuit representing the VSC and its load is shown.

Another VSC working in current mode connected to the DC-link is considered as load. Although other loads could be considered (current source, RL impedance, etc.), the proposed kind of load is preferred since it often appears in industrial applications. Moreover, this load is very exigent because it presents high frequency harmonics. For this reason, the challenge in controlling the VSC loaded with another VSC is more interesting to test the proposed control strategy.

2.2. VSC input-output feedback linearization

When currents are selected as outputs, the vector relative degree equals to two. However, the model order is three and consequently an internal dynamics appears. As mentioned in the introduction, when the DC-voltage and the direct-axis current are chosen as outputs, simplifications can be proposed for obtaining a control law without internal dynamics. Nevertheless, in order to obtain a control law without neither internal dynamics nor model simplifications, the selection of other outputs must be considered. Our proposal consists of choosing outputs allowing to obtain vector relative degree equal to three. To this end, an energy function e_c (relative degree equal to two) is selected as output (see below $\mathbf{h}(\mathbf{x})$ definition). The DC-voltage will be controlled by using this output. In addition, direct-axis current i_d (relative degree equal to one) will be controlled to maintain the input unity power factor. By choosing these outputs, the vector relative degree is equal to three. In this way, internal dynamics is avoided. Consequently, complete feedback linearization is obtained and two linear and decoupled loops are to be controlled.

Feedback linearization is applied to the model given by (1)-(3). The system in matrix form becomes,

$$\dot{\mathbf{x}} = \mathbf{f}(\mathbf{x}) + \mathbf{G}(\mathbf{x})\mathbf{u},\tag{4}$$

$$\mathbf{y} = \mathbf{h}(\mathbf{x}),\tag{5}$$

where

$$\mathbf{f} = \begin{bmatrix} -\frac{R}{L}\mathbf{i}_d - \omega\mathbf{i}_q - \frac{v_d}{L} \\ -\frac{R}{L}\mathbf{i}_q + \omega\mathbf{i}_d - \frac{v_q}{L} \\ \frac{i_l}{C_{dc}} \end{bmatrix}, \quad \mathbf{G} = [\mathbf{g}_1 \ \mathbf{g}_2] = \begin{bmatrix} \frac{v_{dc}}{L} & \mathbf{0} \\ \mathbf{0} & \frac{v_{dc}}{L} \\ -\frac{3i_d}{2C_{dc}} & -\frac{3i_q}{2C_{dc}} \end{bmatrix},$$
$$\mathbf{h} = \begin{bmatrix} h_1 \\ h_2 \end{bmatrix} = \begin{bmatrix} e_c \\ i_d \end{bmatrix} = \begin{bmatrix} \frac{l}{2} \left(i_d^2 + i_q^2 \right) + \frac{C_{dc}}{2} v_{dc}^2 \\ i_d \end{bmatrix},$$
$$\mathbf{x} = [i_d \ i_q \ v_{dc}]^T, \quad \mathbf{u} = [\eta_d \ \eta_a]^T, \quad \mathbf{y} = [e_c \ i_d]^T,$$

where **x**, **y**, and **u** are the state, output and input vectors of the nonlinear system, respectively. The control law linearizing the system (4) results (Eq. (6.96) in [32]),

$$\left[\eta_d \ \eta_q\right]^T = \mathbf{E}^{-1}(\mathbf{x}) \left(\left[u_e \ u_d \right]^T - \boldsymbol{\alpha}(\mathbf{x}) \right), \tag{6}$$

where¹

$$\mathbf{E} \triangleq \begin{bmatrix} L_{\mathbf{g}_1} L_{\mathbf{f}} h_1 & L_{\mathbf{g}_2} L_{\mathbf{f}} h_1 \\ L_{\mathbf{g}_1} h_2 & L_{\mathbf{g}_2} h_2 \end{bmatrix}, \quad \boldsymbol{\alpha} \triangleq \begin{bmatrix} L_{\mathbf{f}}^2 h_1 \\ L_{\mathbf{f}} h_2 \end{bmatrix}.$$
(7)

By assuming $v_d = \dot{v}_d = \dot{v}_q \cong$ 0, for the voltage-source converter model results,

$$\boldsymbol{\alpha}(\mathbf{X}) = \left[\frac{2}{3C_{dc}}\dot{i}_{L}^{2} + \frac{2R^{2}}{L}\left(\dot{i}_{d}^{2} + \dot{i}_{q}^{2}\right) + \frac{2}{3}\dot{i}_{L}\boldsymbol{\nu}_{dc} + \frac{\boldsymbol{\nu}_{q}}{L}\left(3R\dot{i}_{q} + \boldsymbol{\nu}_{q} - L\omega\dot{i}_{d}\right) - \frac{R}{L}\dot{i}_{d} - \omega\dot{i}_{q}\right],$$

 $[\]mathbf{E}(\mathbf{x}) =$

¹ The simplified notation within Lie derivative has been used [32]. Here, $L_{\mathbf{f}}^n \lambda$ denotes the λ Lie derivative of *n* order with respect to **f**.

Note that the matrix **E** is singular when

$$\beta = v_{dc} (i_L i_q L + C_{dc} v_{dc} (v_q + 2Ri_q)) = 0.$$
(8)

A detailed analysis of variables values allows to determine that the function β does not become zero in the converter operation range. Notice that *R* is small and consequently $2 Ri_q$ can be neglected against v_q . For this reason, $C_{dc}v_{dc}(v_q + 2Ri_q)$ is always positive. To our convention i_L and i_q present the same sign so that $i_L i_q$ is positive. Consequently, β is positive. During transient, when power sign is changing both currents are crossing zero. Therefore, $i_L i_q$ could have negative sign because currents could have opposite directions. However, in such cases absolute values of the currents are near zero. Then $i_L i_q L \ll C_{dc} v_{dc} v_q$ and consequently β is also positive in this case.

By using (6), the system in the transformed domain becomes linear. It is expressed as,

$$\ddot{e}_c = u_e,\tag{9}$$

$$\dot{l}_d = u_d. \tag{10}$$

2.3. Power factor control

The power factor is controlled by i_d . The tracking error is defined as $e_d = i_d - i_d^*$, thus the tracking error dynamics results $\dot{e}_d + \gamma_1 e_d + \gamma_2 \int e_d dt = 0$, where an integral term has been added to provide robustness against parametric uncertainties. From this equation, and using (10) the auxiliary control value u_d is calculated, becoming,

$$u_d = \dot{i}_d^{\star} - \gamma_1 \left(i_d - i_d^{\star} \right) - \gamma_2 \int \left(i_d - i_d^{\star} \right) dt, \qquad (11)$$

where the reference value is set to zero $(i_d^* = 0)$ for guaranteeing unity power factor. The gains γ_1 and γ_2 can be designed by using linear techniques (poles assignment, linear quadratic regulator, etc.).

2.4. DC-voltage control

The voltage v_{dc} in the DC-link will be controlled through the energy function e_c . By defining the tracking error as $e_e = e_c - e_c^{\star}$, the tracking error dynamics becomes, $\ddot{e}_e + \lambda_1 \dot{e}_e + \lambda_2 e_e = 0$. Thus, the auxiliary input u_e results,

$$u_e = \ddot{e}_c^{\star} - \lambda_1 (\dot{e}_c - \dot{e}_c^{\star}) - \lambda_2 (e_c - e_c^{\star}), \qquad (12)$$

where (9) was used.

From energy definition, power balance, and unity power factor condition the following equalities are obtained,

$$\boldsymbol{e}_{c}^{\star} = \frac{L}{2} \left(i_{d}^{\star 2} + i_{q}^{\star 2} \right) + \frac{C_{dc}}{3} v_{dc}^{\star 2}, \tag{13}$$

$$\dot{e}_{c}^{\star} = p_{c}^{\star} = \frac{2}{3} i_{L} v_{dc}^{\star} - R \left(i_{d}^{\star 2} + i_{q}^{\star 2} \right) - v_{q} i_{q}^{\star} = 0,$$
(14)

$$i_d^{\star} = 0. \tag{15}$$



Fig. 1. Electrical circuit of a three-phase voltage-source converter and load.



Fig. 2. Block diagram of the proposed control strategy.

These equations are used for calculating the static energy reference needed to maintain the DC-voltage in a desired value. Then,

$$e_{c}^{\star} = \frac{L}{2} \left(\sqrt{\left(\frac{\nu_{q}}{2R}\right)^{2} + \frac{2}{3}} \frac{i_{L} \nu_{dc}^{\star}}{R} - \frac{\nu_{q}}{2R} \right)^{2} + \frac{C_{dc}}{3} \nu_{dc}^{\star 2}, \tag{16}$$

where v_{dc}^{\star} stands for the DC-voltage reference, and it is assumed that the load current is known. Note that the energy reference depends on model parameters. For this reason, a dynamical compensation is added to e_c^{\star} in order to improve the robustness against model parameters uncertainties. This results in,

$$e_{c \ dyn}^{\star} = e_{c}^{\star} - \rho_{1}(\nu_{dc} - \nu_{dc}^{\star}) - \rho_{2} \int (\nu_{dc} - \nu_{dc}^{\star}) dt.$$
(17)

Note that the reference given by (17) can be split in two parts. One of them (e_c^*) is used to establish the value of the DC-voltage to be controlled. The other one is small in magnitude, since it is only used for correcting the bias appearing in the energy set-point due

to parameters uncertainties. Finally, energy reference given by (17) is used in (12).

3. Load current estimation

The feedback linearization needs the load current and its timederivative to be implemented (see α and **E** definitions in Eq. (6)). A nonlinear reduced-order observer will be constructed for estimating the load current and its time-derivative. This allows avoiding the use of a current sensor. At the same time it provides a more reliable measure of the current and its derivative, especially when the load current may be discontinuous as is the case of an inverter load.

By arranging the model in a convenient way, it may be rewritten as,

$$\dot{\mathbf{x}} = \mathbf{A}\mathbf{x} + \mathbf{B}_u(\mathbf{x}, \mathbf{u}) + \mathbf{B}_v i_L, \tag{18}$$



Fig. 3. Active power consumed by the VSC, a-phase current, a-phase voltage and DC-link voltage deviations in presence of disturbances.

where

$$\mathbf{A} = \begin{bmatrix} -\frac{R}{L} & -\omega & \mathbf{0} \\ \omega & -\frac{R}{L} & \mathbf{0} \\ \mathbf{0} & \mathbf{0} & \mathbf{0} \end{bmatrix}, \quad \mathbf{B}_p = \begin{bmatrix} \mathbf{0} \\ \mathbf{0} \\ \frac{1}{C_{dc}} \end{bmatrix},$$
$$\mathbf{B}_u(\mathbf{x}, \mathbf{u}) = \begin{bmatrix} \frac{\eta_d v_{dc}}{L} - \frac{v_d}{L} \\ \frac{\eta_q v_{dc}}{L} - \frac{v_q}{L} \\ -\frac{3}{2C_{dc}}(\eta_d i_d + \eta_q i_q) \end{bmatrix}.$$

By assuming softly varying load current the system may be extended to,

$$\begin{bmatrix} \dot{\mathbf{x}} \\ \dot{i}_L \\ \dot{d}_L \end{bmatrix} = \begin{bmatrix} \mathbf{A} & \mathbf{B}_p & \mathbf{0} \\ \mathbf{0} & 0 & 1 \\ \mathbf{0} & 0 & 0 \end{bmatrix} \begin{bmatrix} \mathbf{x} \\ \dot{i}_L \\ d_L \end{bmatrix} + \begin{bmatrix} \mathbf{B}_u \left(\mathbf{x}, \mathbf{u} \right) \\ \mathbf{0} \\ 0 \end{bmatrix},$$
(19)

where d_L stands for the load current time-derivative.

For the system represented by (19), a Luenberger nonlinear reduced-order observer is given as shown in [33],

$$\dot{\boldsymbol{\xi}} = \mathbf{A}_r \boldsymbol{\xi} + \mathbf{B}_r + \mathbf{A}_r \mathbf{G}_p \mathbf{x},\tag{20}$$

 $[\hat{\boldsymbol{i}}_L \ \hat{\boldsymbol{d}}_L]^T = \boldsymbol{\xi} + \boldsymbol{\mathsf{G}}_p \boldsymbol{\mathsf{x}},\tag{21}$

with,

$$\mathbf{A}_{r} = \begin{bmatrix} \mathbf{0} & \mathbf{1} \\ \mathbf{0} & \mathbf{0} \end{bmatrix} - \mathbf{G}_{p} [\mathbf{B}_{p} \quad \mathbf{0}], \tag{22}$$

$$\mathbf{B}_{r} = -\mathbf{G}_{p}(\mathbf{A}_{r}\mathbf{x} + \mathbf{B}_{u}(\mathbf{x}, \mathbf{u})), \tag{23}$$

where $\xi \in \mathbb{R}^{2 \times 1}$ is the observer state vector, and $\mathbf{G}_p \in \mathbb{R}^{2 \times 3}$ is the observer gain matrix, designed as a trade-off between measurement noise and estimation error convergence rate. Although the observer structure is nonlinear, the error dynamics of the proposed observer



Fig. 5. Scheme used for robustness test.

is linear and it is governed by the following expression $\dot{\mathbf{e}} = \mathbf{A}_r \mathbf{e}$. Therefore, the gain \mathbf{G}_p can be calculated by linear techniques, like pole placement, quadratic optimization techniques, etc. In this way, global exponential speed of convergence is obtained.

By applying this technique to the VSC, the observer obtained via (20) and (21) results,

$$\begin{split} \dot{\xi}_{1} &= \frac{g_{13}}{C_{dc}} \left(-\xi_{1} - g_{13} \, \nu_{dc} + \frac{3}{2} (\eta_{d} i_{d} + \eta_{q} i_{q}) \right) + \xi_{2} + g_{23} \, \nu_{dc}, \\ \dot{\xi}_{2} &= \frac{g_{23}}{C_{dc}} \left(-\xi_{1} - g_{13} \, \nu_{dc} + \frac{3}{2} (\eta_{d} i_{d} + \eta_{q} i_{q}) \right), \end{split}$$
(24)

where the estimated load current and its time-derivative are calculated as,

$$i_L = \xi_1 + g_{13} v_{dc}, \tag{25}$$

$$d_L = \xi_2 + g_{23} v_{dc}, \tag{26}$$

where the scalar constants g_{13} and g_{23} are entries of the gain matrix G_p . Note that observer expressions given by (24)–(26) are simple enough, such that they can be implemented in commercially available DSPs.

A block diagram illustrating the proposed control strategy with load current estimation is shown in Fig. 2. Note that proposed strategy is a nonlinear law where estimates provided by an observer are included. In the linear case, closed-loop stability (observer-



Fig. 4. Actual current and estimated load current and its time-derivative.

2004

based controller plus plant) is guaranteed by using Separation Theorem [34]. Consequently, the control law and observer convergence speed can be fixed independently. The Separation Theorem does not apply in nonlinear cases. For this reason, in order to choose the observer gains some conditions should be considered to guarantee stability. It is possible to find papers establishing sufficient conditions to guarantee asymptotic convergence in the nonlinear case (see for instance [35–37]). In our case, the observer can be designed proceeding as follows. First, the nonlinear control strategy is calculated by assuming the true states are available. Then, observer gains are selected satisfying the sufficient conditions presented in [38]. These conditions establish how fast must be the observer convergence for guaranteeing closed-loop stability.

4. Performance testing

The proposed control was evaluated through several tests, in this Section the most relevant results are presented. The VSC and the controller were implemented in the SimPowerSystems blockset of SIMULINK/MATLABTM. Controllers gains were set to: $\gamma_1 = 1000 \text{ s}^{-1}$, $\gamma_2 = 1 \times 10^5 \text{ s}^{-2}$, $\lambda_1 = 1500 \text{ s}^{-1}$, $\lambda_2 = 3.6 \times 10^5 \text{ s}^{-2}$, $\rho_1 = 0.05 \text{ J V}^{-1}$, and $\rho_2 = 10 \text{ J V}^{-1} \text{ s}^{-1}$, whereas observer gains were

 $g_{13} = 0.2 \text{ A V}^{-1}$, and $g_{23} = 255 \text{ A V}^{-1} \text{ s}^{-1}$. Other data and parameters are shown in the electrical circuit presented in Fig. 1. To obtain a digital controller, the Euler rule was employed for discretizing continuous equations $\dot{x} \simeq \frac{x_{k+1} - x_k}{h}$, where *h* is the sample time, and it was set to 100 µs.

4.1. Dynamic behavior in presence of load changes

In a first test, the load current was varied from zero to nominal value. Then, the power flow direction was suddenly changed for showing the regenerative behavior and the transient response of the proposed controller. In Fig. 3a the consumed active power is illustrated. VSC a-phase current and voltage trajectories are presented in Fig. 3b. It can be seen that both trajectories are in-phase (or contra-phase) every time (unity power factor condition). It is still verified during transients and in presence of sudden changes of active power consumed by the load. This behavior is achieved by using the proposed controller, since the current loop controlling i_d (reactive power) is decoupled. Fig. 3c shows the DC-voltage regulation, where only small deviations are observed. These deviations are smaller than 5.3%, and they last less than 17 ms. This is because the proposed strategy contains nonlinear model information and a feedforward compensation for rejecting the disturbance.



Fig. 6. Parameters uncertainties test.

Due to its predictive action, feedforward compensation allows to reject the disturbance in a fast way. Note that traditional DC-voltage controllers based on PI acts when the error introduced by the disturbance is detected.

The observer performance is presented in Fig. 4. It must be noted that the load current presents high harmonics (see Fig. 4a. This issue must be taken into account when a load current feedforward compensation is to be implemented. In this case, an estimator of the load current mean value is more effective than a direct measurement, since the high harmonics of the load current are not feedforwarded. Fig. 4b and c shows the estimated load current and its estimated time-derivative. These estimates are injected as feedforward terms in the control law for avoiding current sensors.

4.2. Robustness against parameters uncertainties

In order to analyze the robustness of the proposed controller against parameters uncertainties, another line with the same resistance (R) and the same inductance (L) is connected in parallel configuration (see Fig. 5). When this line is connected (at t = 0.06 s) the impedance is reduced to 50%. Then, while this condition is maintained the load power flow direction is changed from the positive nominal value to negative nominal value. In Fig. 6a it can be seen that the proposed strategy allows to reject this disturbance in a good way. Fig. 6b and c shows the active power trajectory and the consumed reactive power. Note that an error appears when the line is connected at t = 0.06 s, but the reactive power value is recovered in a short period of time. It is interesting to remark that at t = 0.13 s a deviation in the reactive power curve occurs. This deviation takes place due to mismatches between the controller and the actual line parameters (in this case, the impedance was reduced to 50%) which avoids the perfect transient decoupling. However, this error is small and it can be asserted that the proposed strategy performs well, taking into account the severe test conditions.

4.3. Voltage sag test

In this subsection a voltage sag test is carried out. The input voltage is reduced to 50% of the nominal value during 100 ms. Fig. 7a illustrates the dynamic response of the DC-link voltage. It remains well regulated except for short transients when the input voltage changes abruptly. In Fig. 7b the a-phase voltage and current evolutions during the sag are shown. The proposed controller maintains current and voltage in-phase attaining unit power factor at every time.

4.4. Behavior under unbalanced conditions

The behavior of the proposed strategy under an asymmetric fault in the electrical network is analyzed. An unbalance equal to 15% in the AC voltage is considered. Fig. 8a shows the converter input voltages where the unbalance is clearly seen. The DC-voltage is shown in Fig. 8b. It is regulated to a constant value, but a ripple appears when the voltage is unbalanced. This is due to the assumption that $v_d = \dot{v}_d = \dot{v}_q \cong 0$, which is not exactly satisfied when the system presents some unbalance. However, the ripple magnitude is less than 2% at 2 ω frequency due to a negative sequence existing in the AC voltage. Nevertheless the control strategy can be improved considering that these terms have a finite value, but this is not justified to correct such a small ripple. Note that voltage and current are in-phase at every time, as shown in Fig. 8c. It can be inferred that although the input voltage is unbalanced, unity power factor is kept.

4.5. Voltage tracking

The last test was constructed for analyzing the controller performance under voltage tracking situation. In some applications, the DC-voltage must be varied for adjusting it to a given value. In order to test this situation, the DC-voltage reference is varied from 4.3 kV to 5 kV, while the load demands the nominal power. Fig. 9



Fig. 7. Voltage sag test.





illustrates the transient and steady state behavior. Note that the proposed strategy performs very well.

5. Conclusions

In this paper a new control strategy for VSCs is introduced. The proposed controller combines a feedback law based on feedback linearization with a feedforward compensation from estimated load current. The main advantages are: outputs are chosen such that complete feedback linearization, without model simplifications and avoiding internal dynamics, is obtained. In this way, linear control laws can be used to control two decoupled subsystems avoiding the slow response caused by the DC-voltage cascade control. Also, unity power factor, high-performance DC-voltage control, and bidirectional power flow are attained. In addition, feedforward compensation allows to improve the performance, when the capacitor size is considerably reduced. The discretized control law is robust against parameters uncertainties, measurement filters delays, high frequencies due to PWM techniques and input AC voltage disturbances.

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